ABSTRACT

The invention provides methodology and processes that extract, separate, filter, and/or transform internally generated events deriving from electronic architectures such as server systems. The internally generated events may for example include chassis logs associated with one or more entities within the electronic architecture. The methodology also preferably transforms chassis logs (typically in binary format) to a text string. The text strings define one or more problems of the electronic architecture. The text strings are input to a series of analyzers corresponding to the series of entities within the architecture. The text strings define a problem detail file and a sequence of chassis codes linked to issues (e.g., problems or system health) within the architecture. The invention thus includes methodology to analyze the text strings, and to generate language statements representative of one or more chassis codes. By way of example, the language statements may be in the form of English statements providing an explanation of the problems experienced by the electronic architecture and/or by the individual entities.